

# Decoding Software Defined Vehicles: Insights, Innovations, and Impact

**Rameshbabu S**

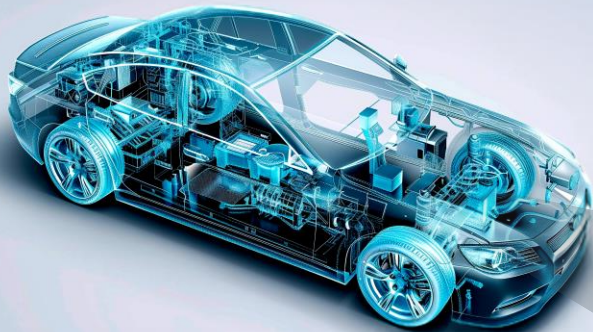
Chief Engineer, Software Defined Mobility

---



**Purposeful. Agile. Innovation.**

# Future of Software Defined Vehicles (SDV) : Exciting Journey !



SDVs are vehicles where most of their functions and features are enabled through software, rather than controlled by hardware components.

The future of driving is smarter, safer, and always improving

Think of your Smartphone but as a Car



## EXPLOSIVE GROWTH BY 2030

**\$650+ Billion**

Total Value Creation



**15-20%**

Automotive Revenue Share



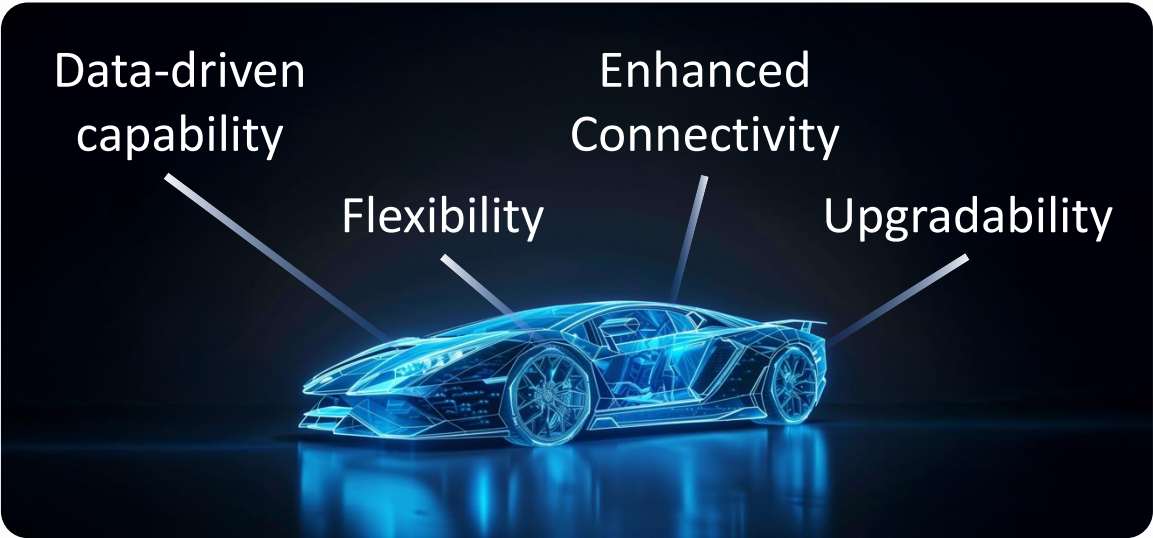
**\$248B**

OEM Revenue



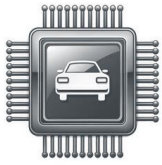
**\$411B**

Supplier Market

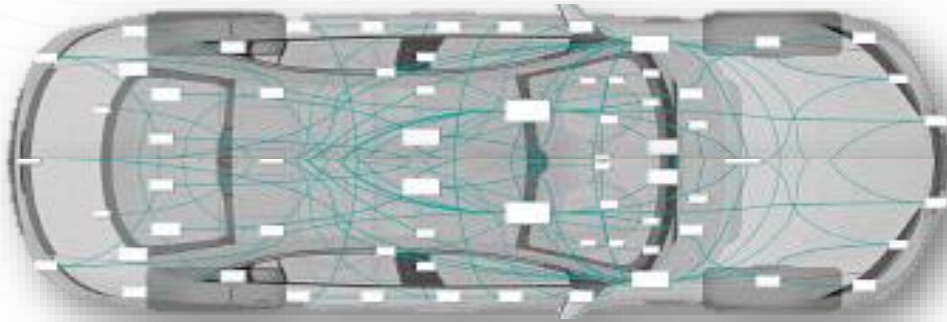


# SDV Platform Success Factors: An OEM Perspective and Motivation

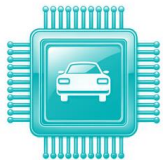
## Legacy Architecture



100-150 ECUs



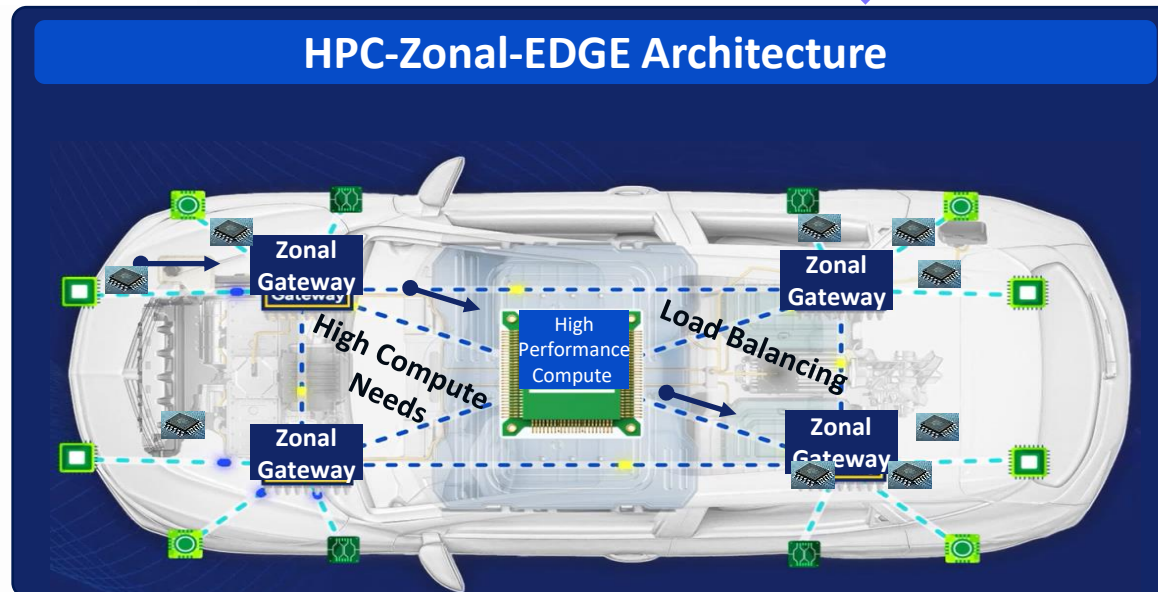
40-50% Reduction



50-60 ECUs

1/3<sup>rd</sup> Supplier  
Count Reduction

## HPC-Zonal-EDGE Architecture



### Harness Optimization

Reduce harness length, weight, and cost (Upto 30%)

### Dynamic Load Balancing

Transfer load dynamically to free cores

### Hardware Consolidation

Multiple function can be served by single Zonal Control



# SDV Platform Success Factors: An OEM Perspective : Feature As A Service (FAAS)

“

## Feature-As-A-Service

is a strategy to diversify OEMs revenue stream through monetizing features



**Faster Go-To-Market (GTM)**



**Continuous Revenue Stream**



**Vehicle Price Reduction**

Nearly, half would keep their vehicles longer if FAAS are available

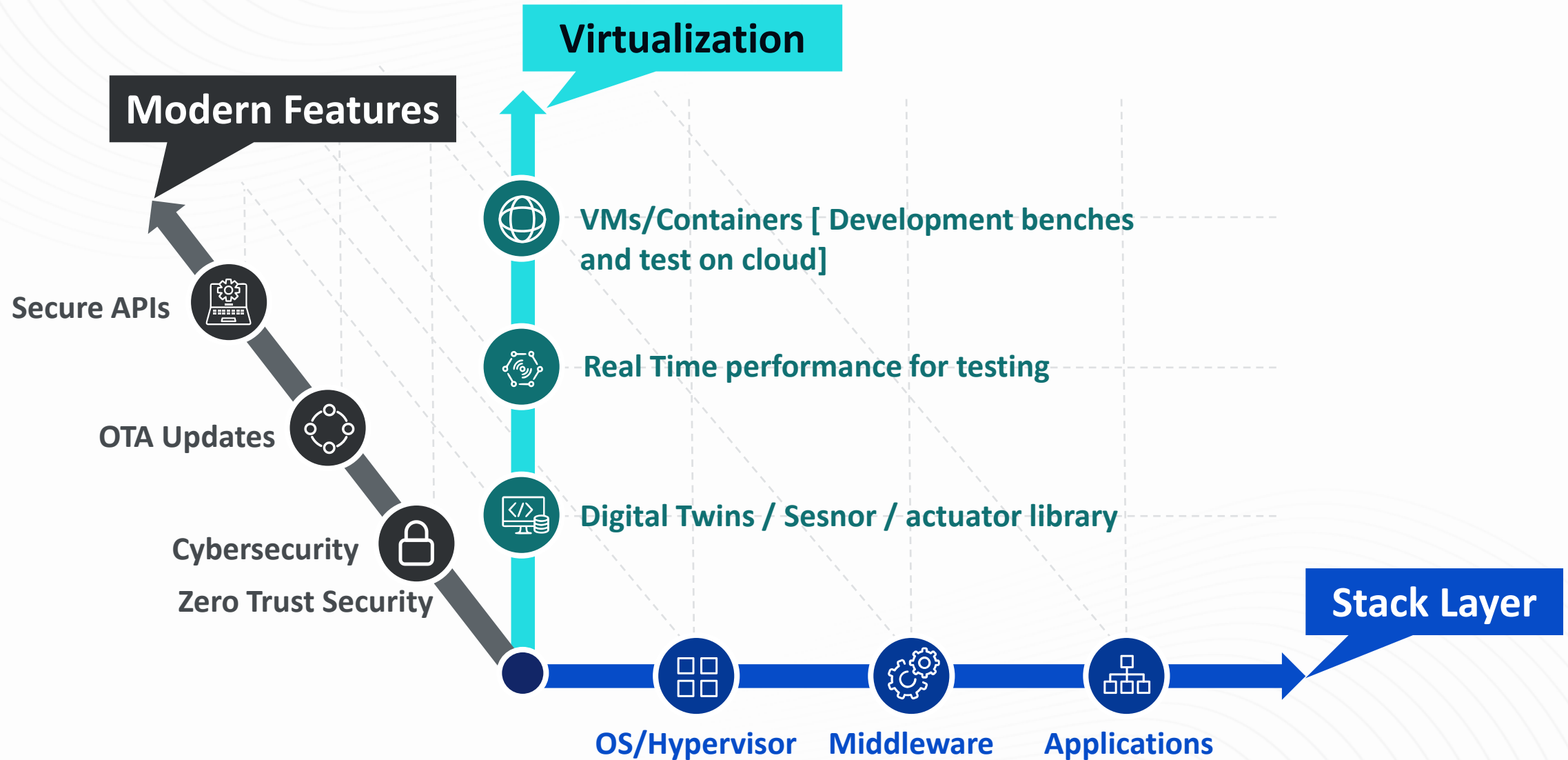
**49%**



**40%**

Likelihood of purchasing next vehicle from brands that offer FAAS

# Three critical dimensions of Software Defined Vehicles



# SDV: Easy Switch between Semiconductor platforms



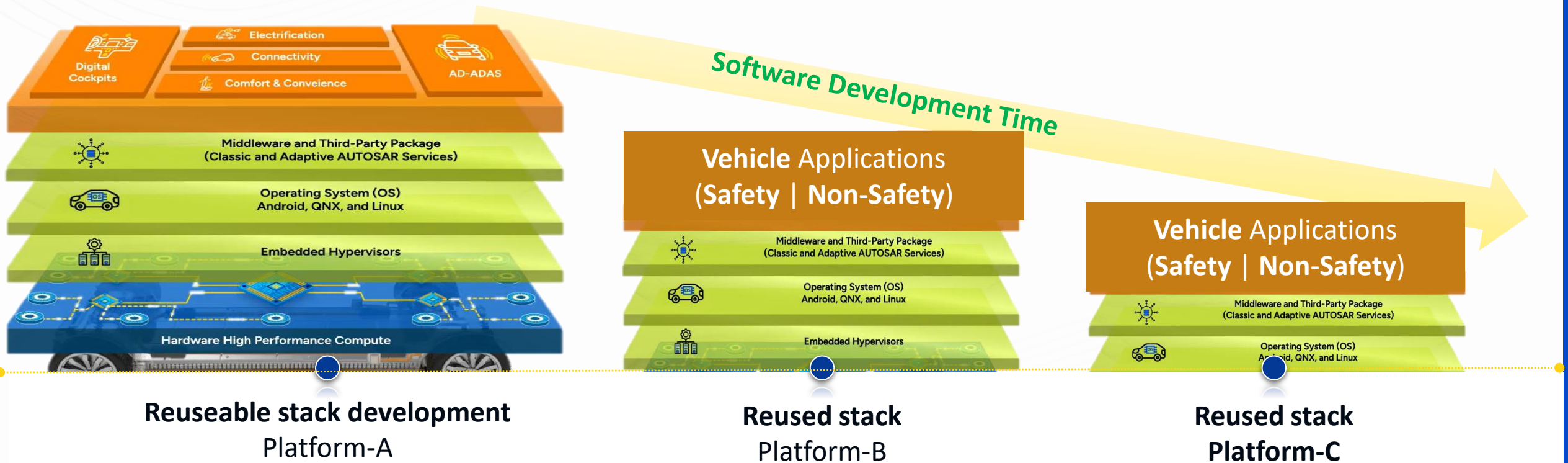
**Easy APP Portability**



**New Platform Support  
In few months**



**Pre-Configured Dev tools on Cloud**



**Stack & Libraries Reuseable Across Semiconductor Platforms**

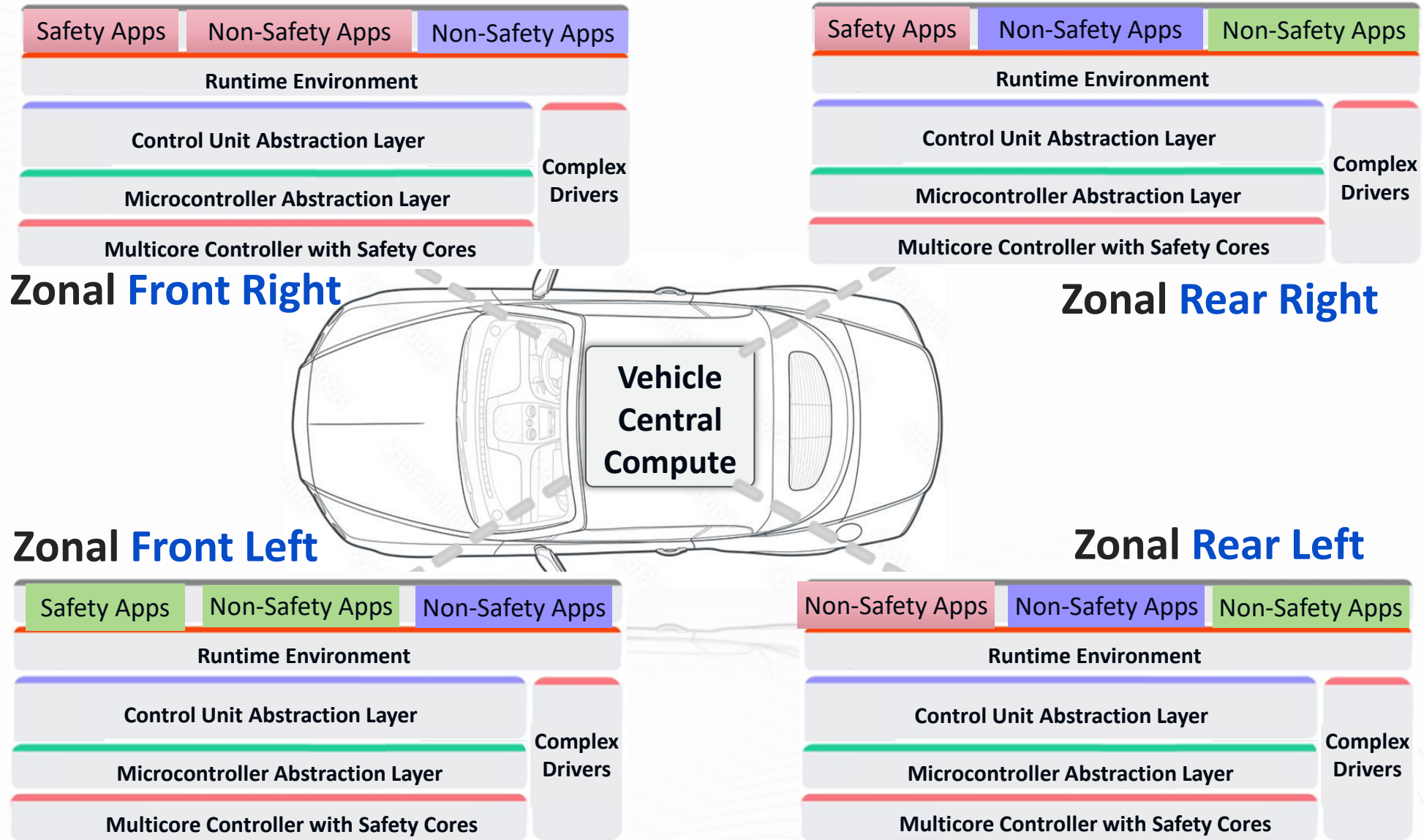


# Smart Zonal Controller [ Reusable Middleware across all Zones ]



**Dynamic Load Balancing with Vehicle Central Compute.**

**Easy to achieve with Ethernet and Some I/P**



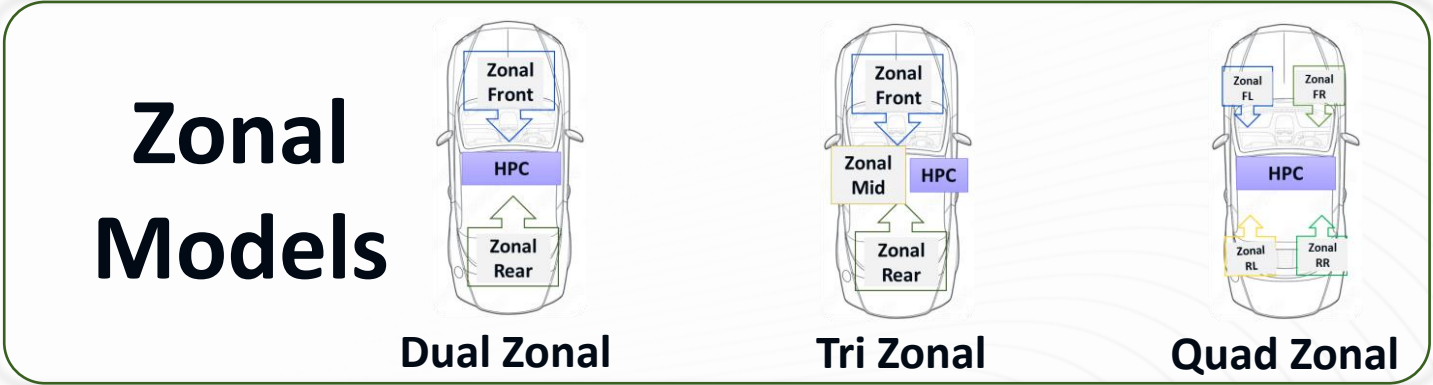
**Manage diverse applications | Shared infrastructure | Enhance modularity | Reduce wiring complexity**

# Zonal Architecture Challenges & Solutions

- Safety And Security**
- Time Synchronization**  
Increase in bandwidth | Combine new computing resources with legacy
- Communication**  
ECUs into a zonal network must cover CAN -> Ethernet backbone -> CAN
- Network Configuration**  
Migrating from single vehicle communication to multi zones
- Latency And Jitter**  
Delays: Processing | Transmission | Scheduling | Queueing

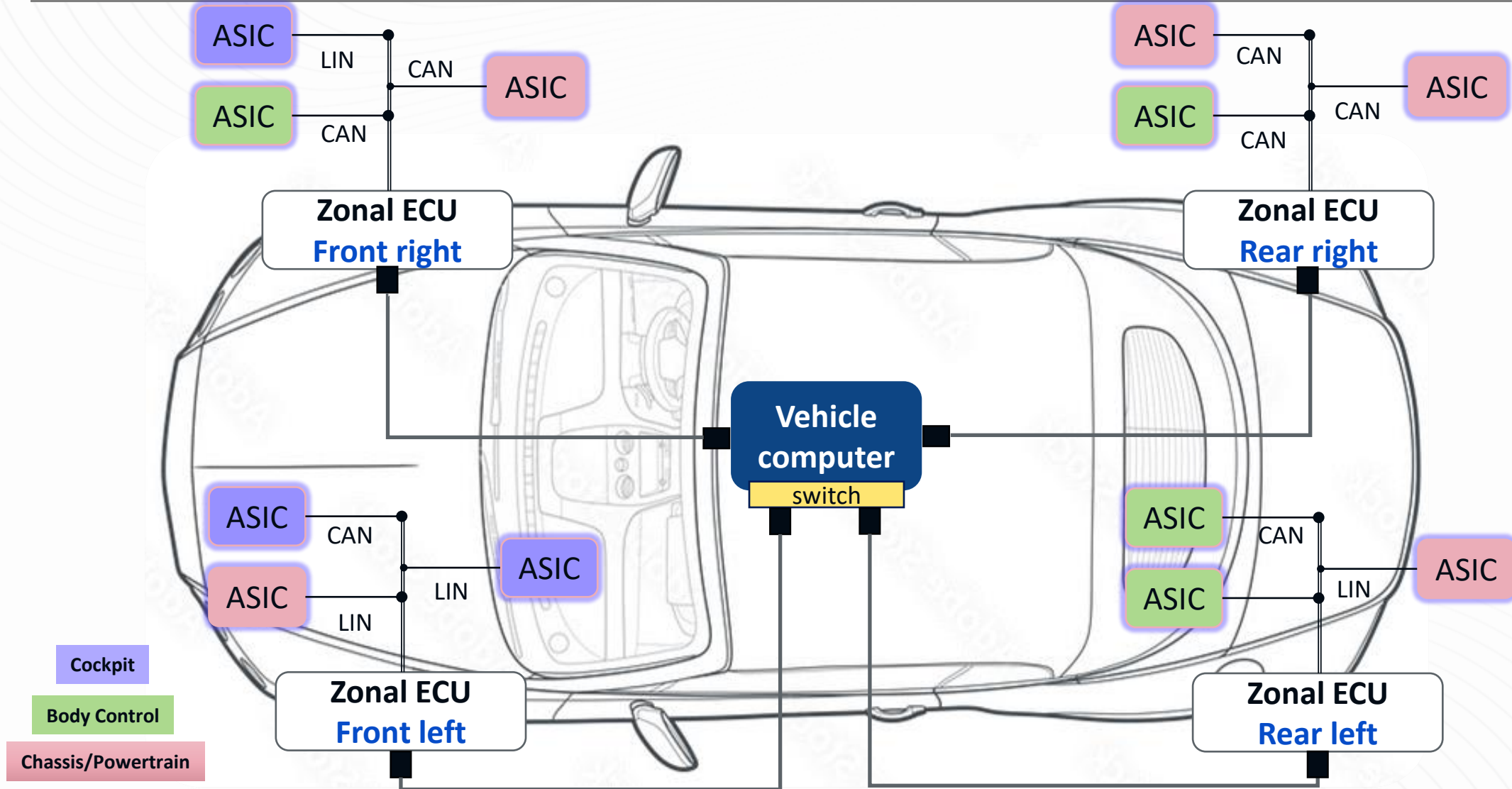


- Time-Division Multiple Access**
- Precision Time Protocol**
- High-speed communication and time-sensitive networking task group (TSN)**
- Software Defined Networking**
- System with high-performance, multi-core processors & advanced task scheduling algos**





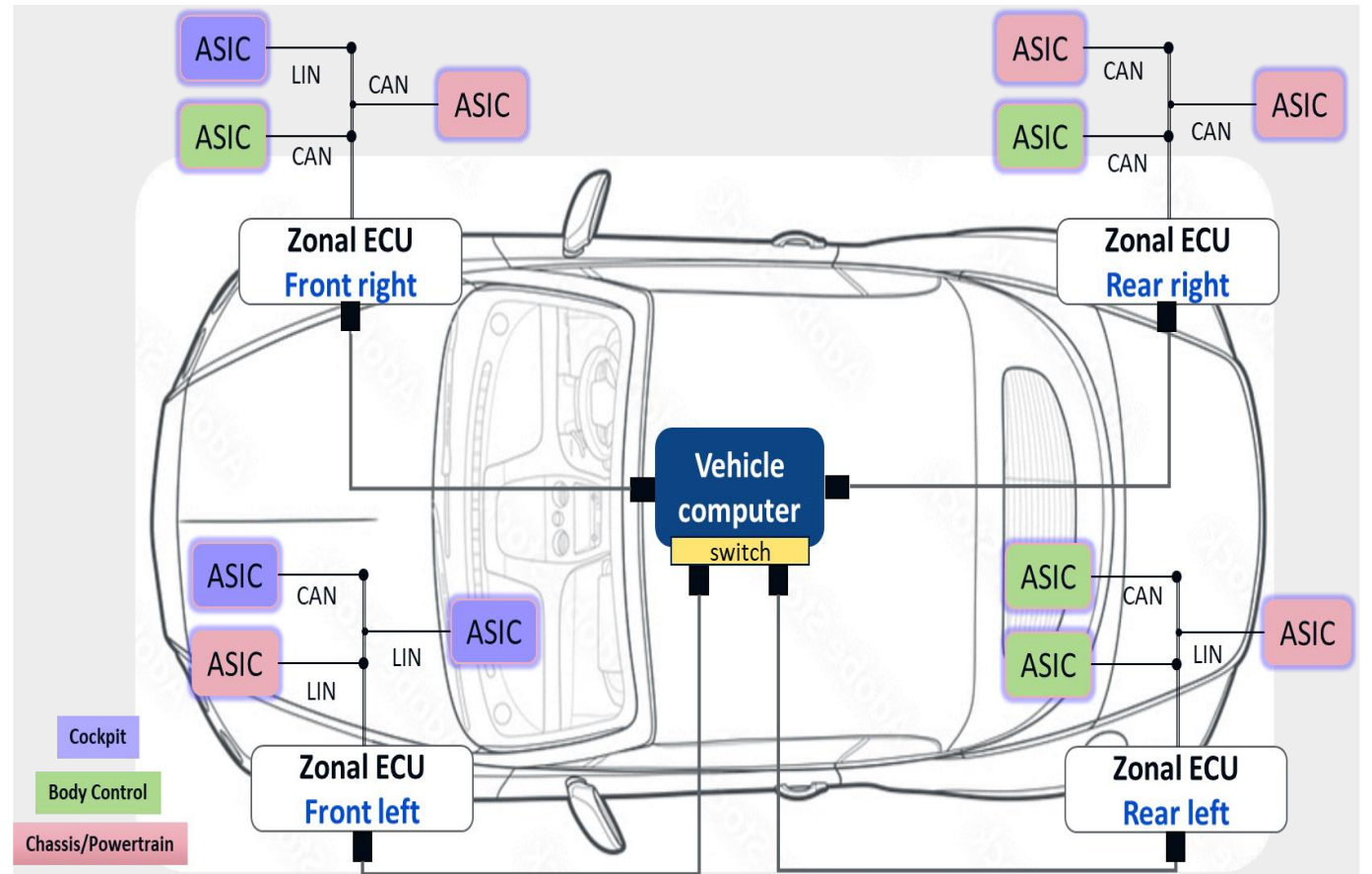
# Edge ECU (Smart ASIC – Sensor and Actuators)



Operates on hardware logic | Do not require firmware | Diagnostics enabled

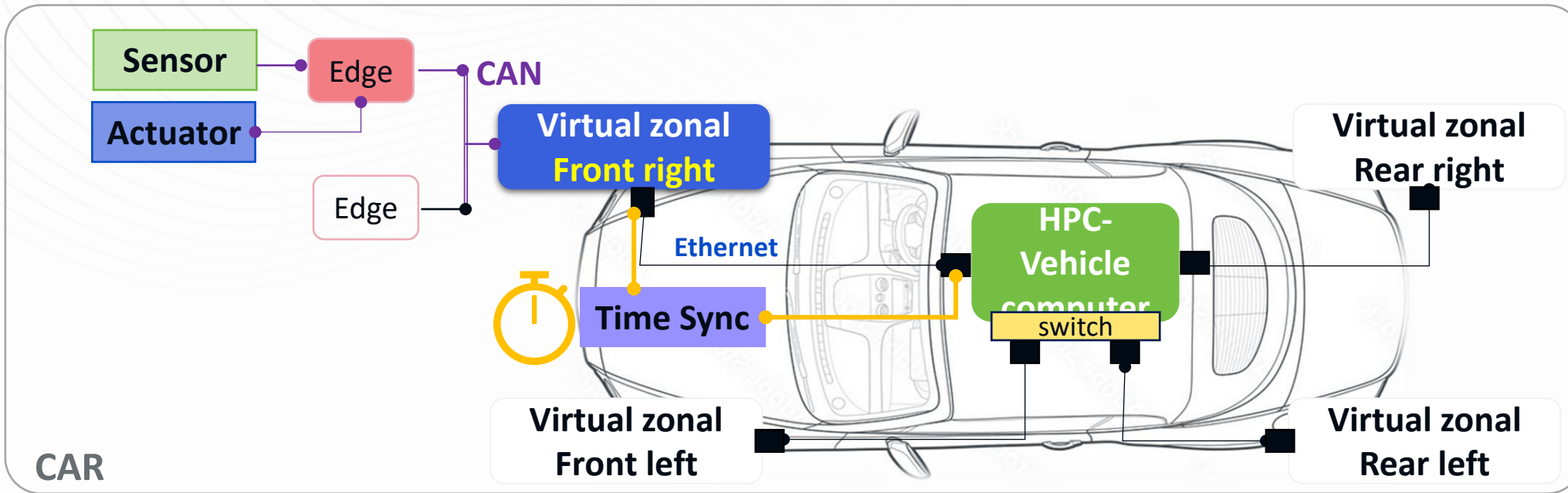
# Smart ASICs to Replace EDGE ECUs: A Micro-less Architecture

- **ASIC-Based Architecture:** Utilizes an ASIC-based architecture for last-mile features and functions (e.g., DOORS ,tail lighting, seat heating, ambient lighting, DOOR handle, Amplifiers, Sun roof).
- **Software Independence:** The architecture eliminates the need for software stacks, bootloaders, and configurators.
- **Lean FOTA Strategy:** Aligns with the lean FOTA strategy required by OEMs.



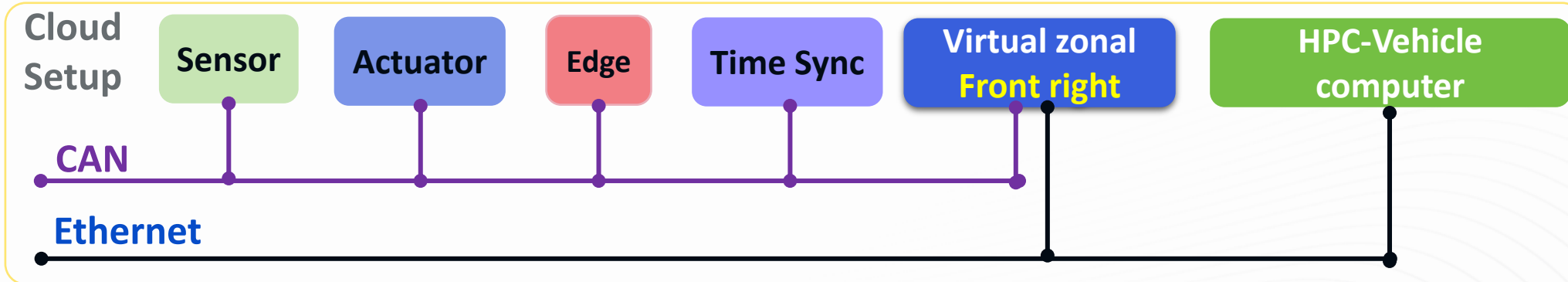
**Operates on hardware logic | Do not require firmware | Diagnostics enabled**

# Virtualization Strategy for Easy Development and Testing



## Assets

- Time Sync
- vComm Bus
- vZonal
- vEdge
- vHPC



Auto Integration Test



Test Reports



# Automotive Security

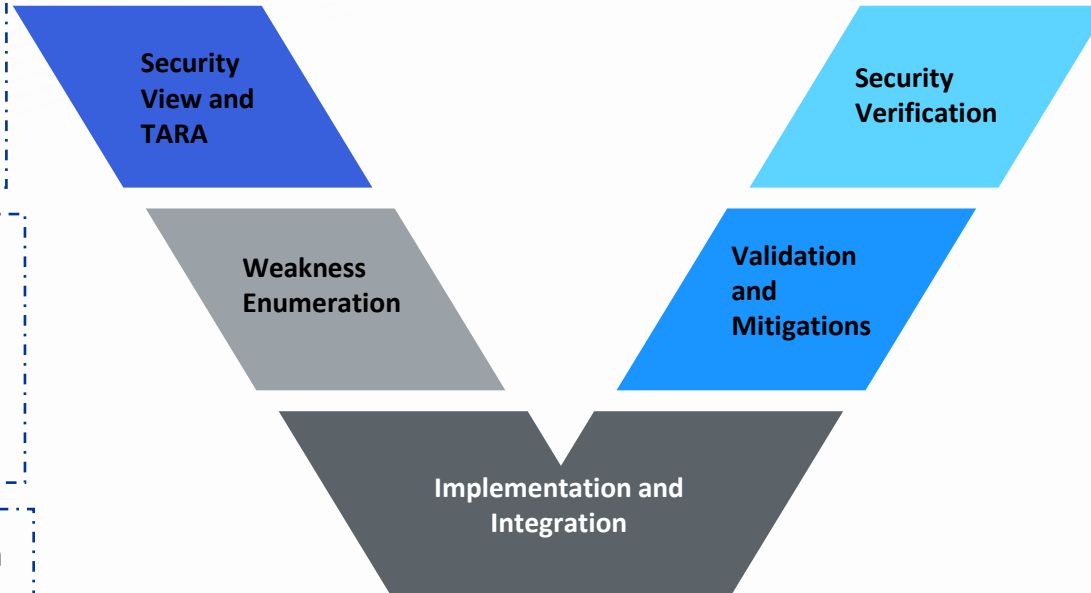
# Preparedness for Addressing Security Concerns

ISO 21434 offering a comprehensive set of guidelines for integrating Automotive Cybersecurity compliances into various phases of automotive development. It stresses on Continuous Assessment, checks on Security Validations and considerations of deploying Low-level Security Features

- Security Goals and Objectives
- System Reconnaissance
- Cybersecurity Management (roles / policies)

- Vulnerability Analysis
- Cybersecurity case and plan
- Interface level, component level vulnerability analysis
- Update Vulnerabilities in tools

- CMAC, HMAC bases Authentication
- AUTOSAR Cryptostack integration
- HSM-based Security Services



- Fuzz Testing
- Security Functional Testing
- Penetration Testing

- Validation of Security Assessment identified vulnerabilities
- Attack vector Analysis
- Design mitigations, create requirements tracing

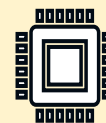
- Key Management
- HSM Crypto integration
- Secure Boot, Secure Flash, SecOC, Secure Diagnostic integration / implementation /



- Security Assessment
- Compliances based on ISO-21434 and UNECE R.155/R.156



- Security Validations (Fuzz Testing, Pen Testing, IDS)
- Security Testing (Functional, Integration Testing)



- HSM Based Security (Secure Boot, Secure Flashing, Secure logging, Secure On-Board Communication, Firewall, Secure Diagnostics, JTAG Protection, Memory (PFlash/DFlash) Protection, TLS, Secure Update ( FOTA/SOTA),
- CAN based secure re-programming, Secure Lifecycle

## Cybersecurity System Validation

### Security Features Testing

### External Interfaces Testing

Secure Boot, Secure ECU modes  
Secure Feature Activation, OS security

IPSec, MacSec, SecOC  
CAN and Ethernet Interface

Transport Layer Security  
Firewall Concept

Certificate download, message  
signing/verifying

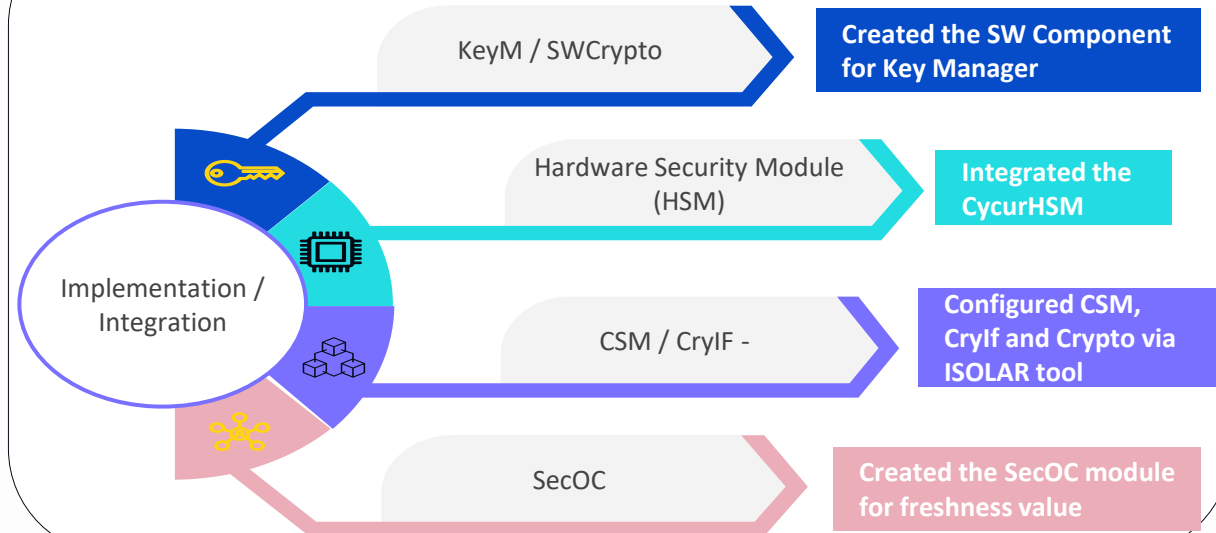
Flash Antiroll back  
Security Key Algorithm for Wi-Fi

Bluetooth  
Wi-Fi

Firewall Concept  
Protection against Physical attacks

Handling security Artifacts  
Handling security Artifacts

## Secure Communication and Key Management



## Secure Boot, Secure Flash, Secure Logging

**BOOT ROM [Boot Manager]**  
Validate Boot Flash - HASH

**BOOT SFLASH [HSM Updater]**  
Validate Secure Image - PKCS

**SECURE IMAGE [HSM SW]**  
Validate Application Bootloader/FBL

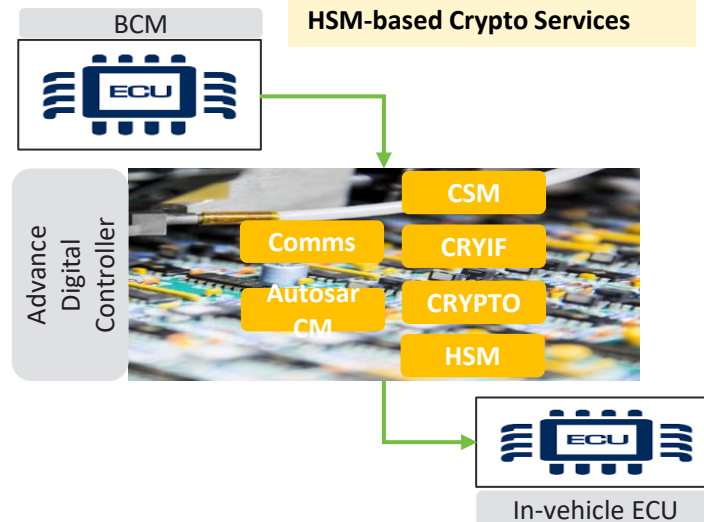
**APPLICATION BOOTLOADER**  
Validate FBL/Application

**APPLICATION SW**

HSM Core

HOST Core

## HSM-based Crypto Services



**Security Assessment and Validation for various Automotive Systems based on ISO 21434 (e.g. Entire Vehicle Level, ECUs like Vehicular Gateway, BCM, Infotainment, Charging Control ECU, Braking Control ECU etc.)**

System Understanding – Item Definition, Asset Identification

Threat Analysis and Risk Assessment

Vulnerability Analysis

Attack Path Analysis and Attack Feasibility Rating

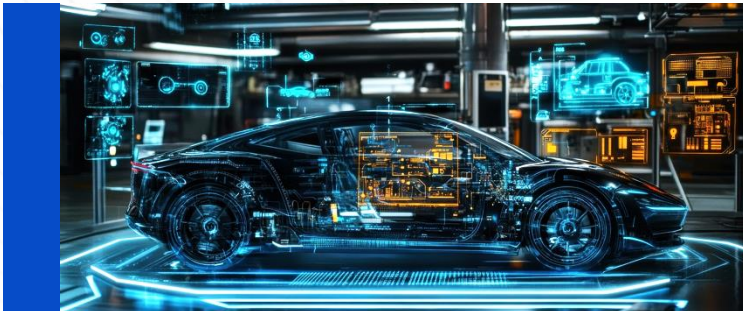
Risk Treatment Determination

Mitigations and Requirement Development

Document preparation at above every stage for Security Assessment

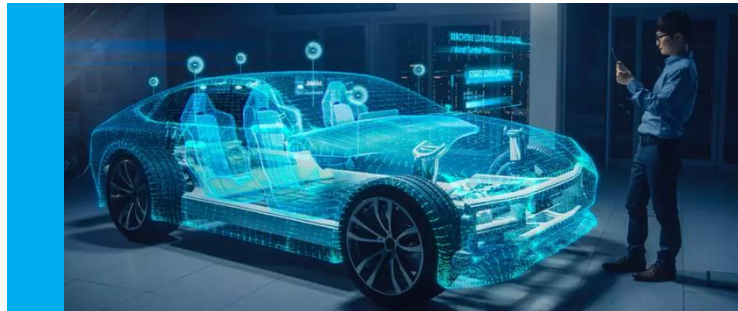


# Technical Realization of Software Defined Vehicles : Right Approach



## Hardware-Software Decoupling

- Flexible Updates
- Faster innovation
- Enhanced Modularity



## Virtualization of HPC/Zonal /EDGE

- Digital Twins
- Test Environments
- Cloud Development



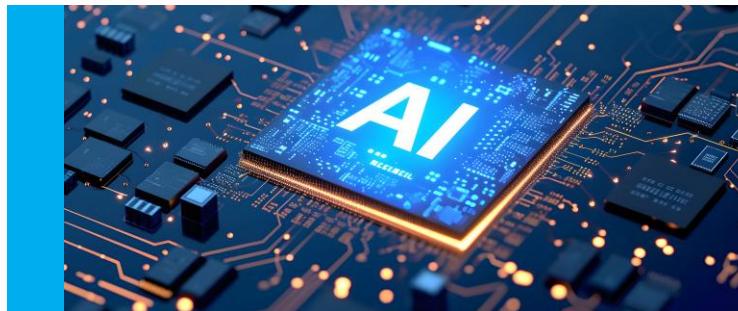
## OTA Updates & Security

- Remote Updates
- Secure Integration
- Real Time Protection



## Function Migration

- Zone Flexibility
- Dynamic allocation
- Seamless Transfer



## Semicon Agnostic Stack

- Hardware Portable
- Multi-Platform
- Minimal Changes



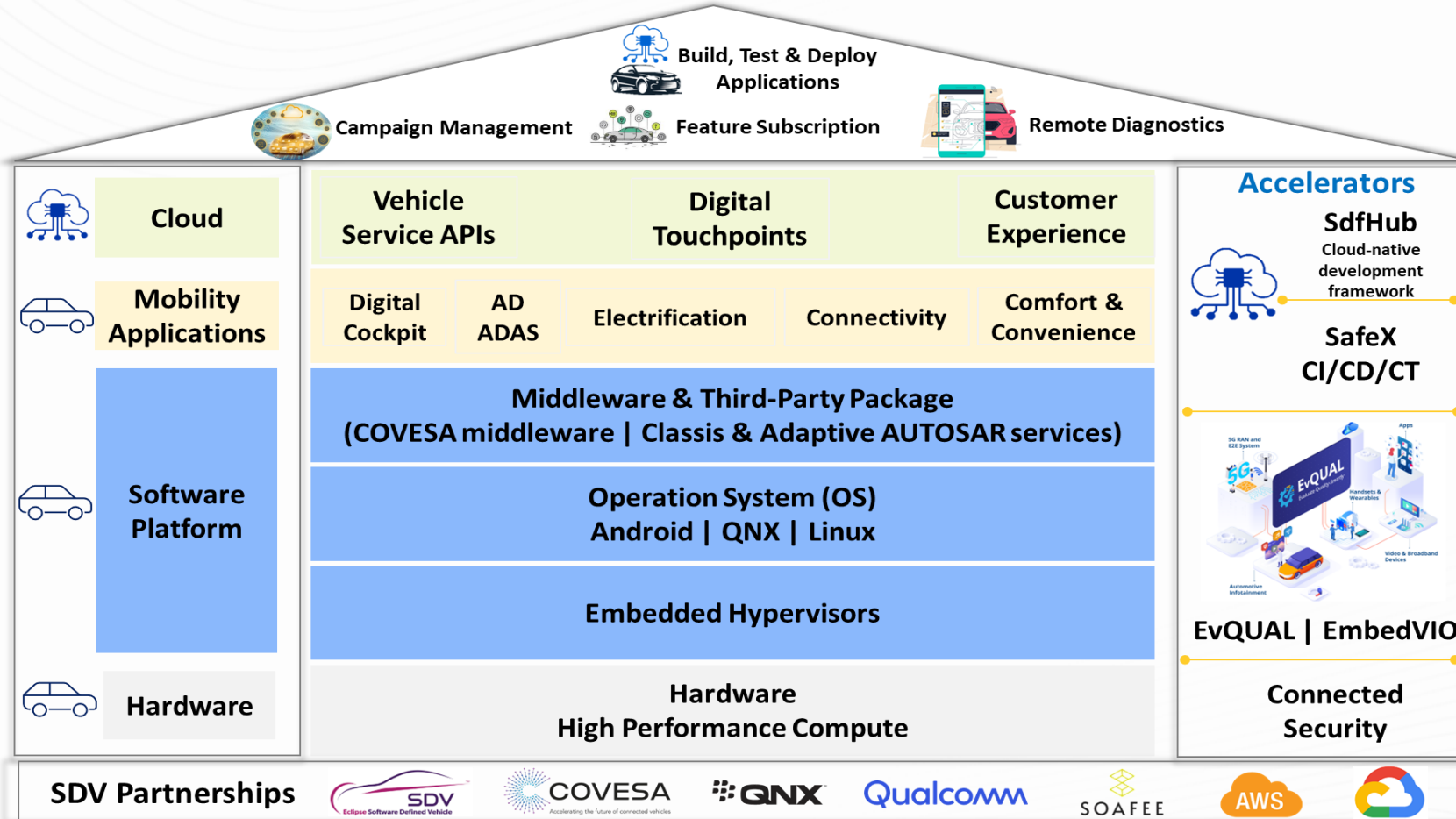
## Industry Collaboration

- Standardization
- Unified Strategies
- Shared Resources
- Open source

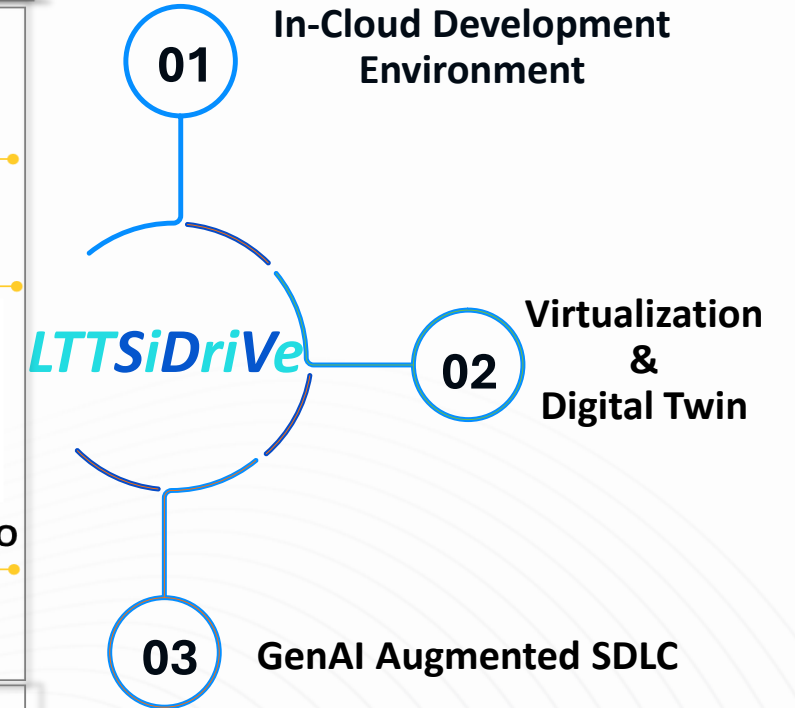
*LTTSiDriVe*



In-house developed integrated solution to adapt and accelerate SDV journey  
**Development | Validation | Deployment**



- ❖ **Semicon Agnostic Platform**
- ❖ **Feature as a Service (FaaS)**





# LTTSiDriVe Framework

- Cloud-native framework enabling development, build and testing of applications on the cloud
- Framework uses virtual targets with environment parity to test systems including AD/ADAS, DCU, Zonal, Edge ECU
- Campaign management (OTA) for software upgrade
- GenAI-based test case generation

## USE CASES



**Auto Light Adaptation based on Ambient Environment**



**Driver Attention Monitoring**



**Green Motion Drive Advisor**

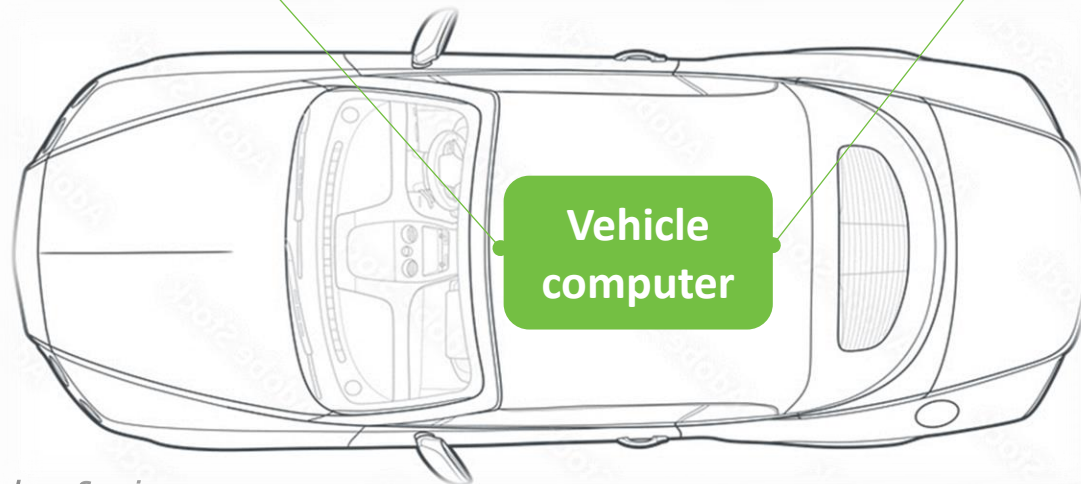
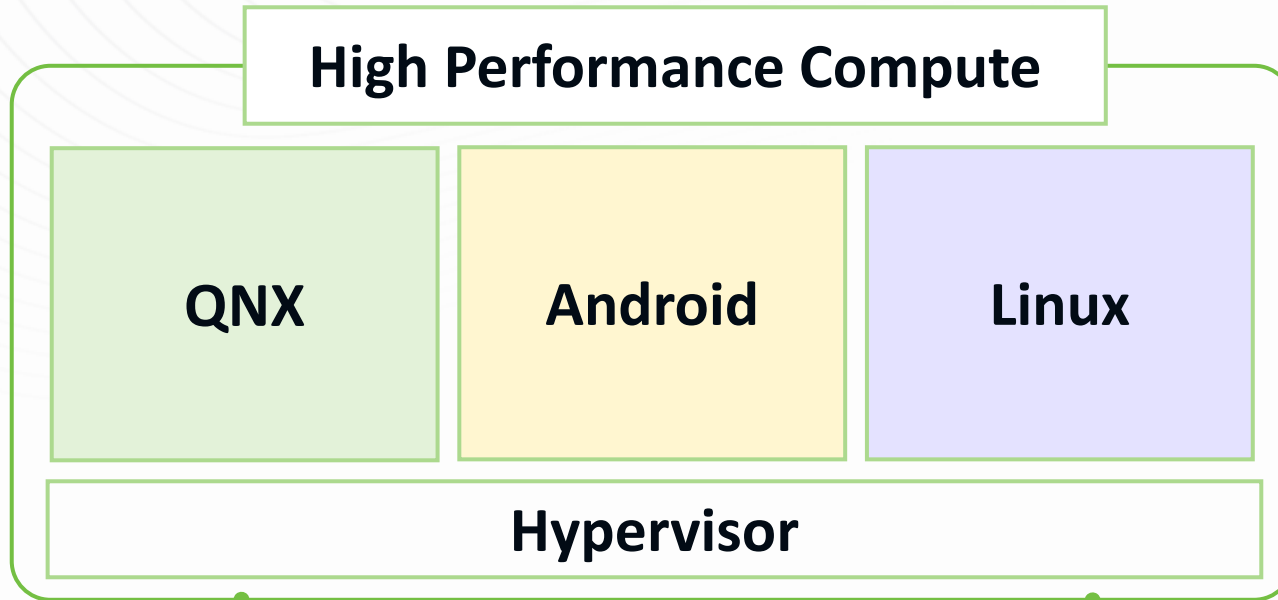


**DoIP [ Vehicle Health and Diagnostics]**



**GenAI-based Interactive In-Vehicle Assistant**





## Use Cases

- **Safety:** Sensing & Actuation
- **Non-Safety:** Personalization



## Success Factors

- Digital Twin libraries and car model
- Libraries: Simulators to test safety and non-safety applications



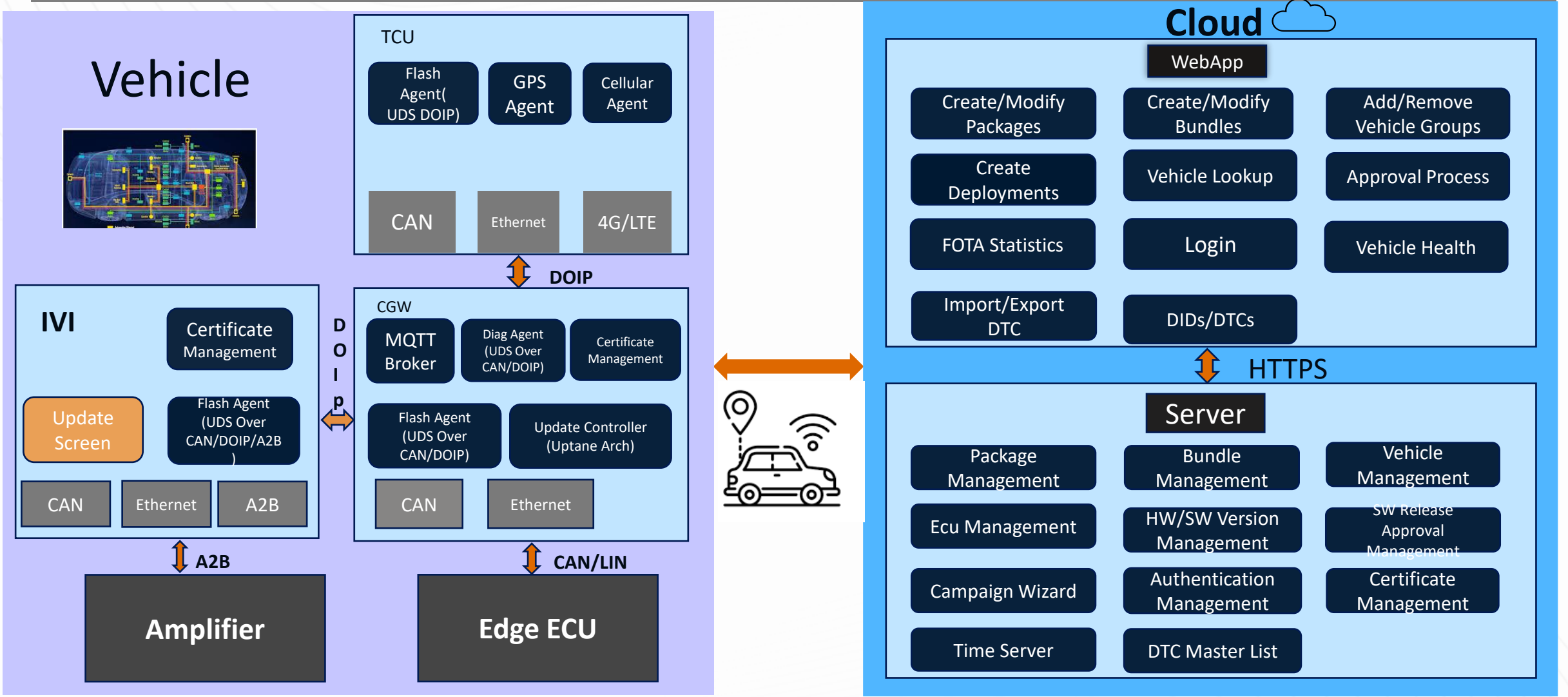
# OTA : Test Rack and Configurable Zonal controllers





# FOTA : Homologated Solution: Cybersecurity / UPTANE compliant Framework

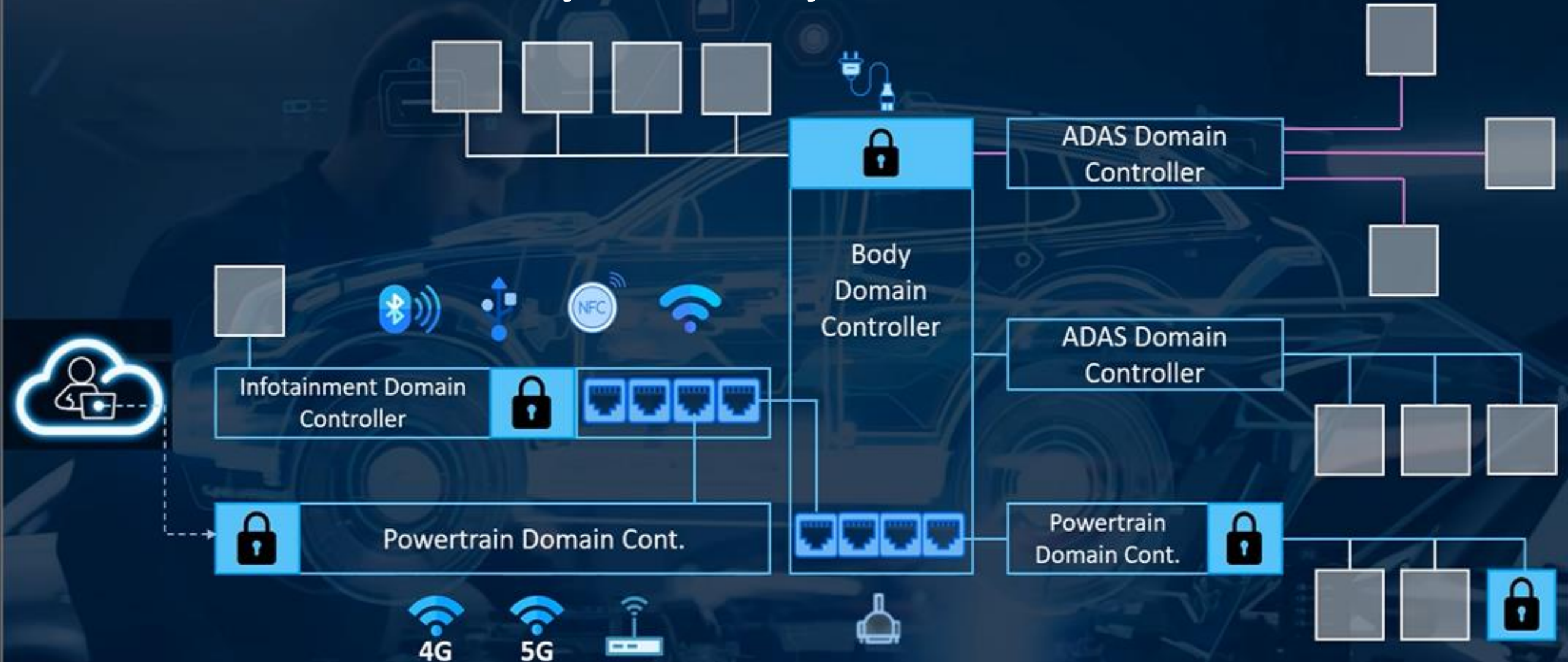
Help leverage *LTTS* experience in OTA deployment



- **Proven and robust OTA solution**, Successfully flashed 20+ major ECUs (BCM, EPS, ESP, TCU, Gateway and more)
  - Campaign management framework for OTA updates with proven mule car and mobile HIL environment

# Automotive Security

# Automotive Cybersecurity Assessment Framework





# Embedded ECU Security Assessment Tool

Compliant to SAE ISO 21434

As part of Automotive Security, LTTS has developed a **Process & Standard Adhered Tool for Embedded ECU Security Assessment**.

This tool focuses on

- **Security Study** (Reconnaissance, Items & Asset Identification, Security View)
- **Security Assessment** (Damage Scenarios, Threats, Impacts, Feasibility, Risk Determination)
- **Security Reporting** (Mitigations, Possible Solutions)



## One-Stop Solution

Study & Assessment Framework, Project Management & Working Repository with Role based secure data Authentication

## Process (ISO 21434) Governance

Enabled with Guidelines and Standards for zero deviation validated workflow supporting Quality Adherence

## Reusability & Trend Study

Reusability in Study & Assessment enabling faster outcome. It enables multi-version studies and Risk Trend Analysis

# Snapshots of CYSAF framework

### Project Details

Project ID: **CySAF-105** Project Name: **Airbag control system**

Project ID	Customer	Project Category	Project Name	Status	Architect		
CySAF-100	Test Customer 1	Layering	Data Structuring Test	Assessment Pending	Amrutha lakshmi G L	Execute	👁️
CySAF-101	Test Customer 2	Interacting	UI/UX Frontend Test	Study Pending	Amrutha lakshmi G L	Execute	👁️
CySAF-102	Test Customer 3	Storing	Data Read/Writing and Compactibility test	Study Pending	V J Leo	Execute	👁️
CySAF-103	Customer 1	Component	Infotainment Unit Usecases	Architect Approval Pending	V J Leo	View	👁️
CySAF-104	Customer 1	Component	Anti lock braking system	Architect Approval Pending	V J Leo	View	👁️
CySAF-105	Customer 2	Component	Airbag control system	Assessment Pending	Amrutha lakshmi G L	Execute	👁️
CySAF-106	Customer 2	Component	TCU- GPS	Study Pending	Amrutha lakshmi G L	Execute	👁️

[Create Project](#)

### System Study

Project ID: **CySAF-105** Project Name: **Airbag control system**

System Reconnaissance Attachment: **Airbag Control System11...**

System Reconnaissance Remark:

#### Architecture of Airbag Control System

[Proceed to Assessment >>](#)

[Upload & View](#)

### System Study

Project ID: **CySAF-105** Project Name: **Airbag control system**

[Add Items](#)

Item ID	Item Name	Item details	Edit
CySAF-I-116	Collision sensors	Item/Component	👉
CySAF-I-117	Pressure Sensor	Collision sensors	👉
CySAF-I-118	Driver Positioning Sensor	Functionality It detects the collision and sends the signal to the airbag chip	👉
CySAF-I-119	PSIS Protocol Communication Medium	Network Interfacing	👉
CySAF-I-120	SPI Protocol Communication Medium	External	👉
CySAF-I-121	CAN Protocol Communication Medium	Operational Environment	👉
CySAF-I-122	LIN Protocol Communication Medium	It detects the collision and sends the signal to the airbag chip	👉
CySAF-I-123	MCU	Product Lifecycle Phase	👉
CySAF-I-124	Squib Drivers	It detects the collision and sends the signal to the airbag chip	👉

[Proceed to Assessment >>](#)

### System Study

Project ID: **CySAF-105** Project Name: **Airbag control system**

System Security View Attachments: **Airbag Control System2...**

System Security View Remark:

Threat Model File:  There is no file. [Upload file](#)

[Proceed to Assessment >>](#)

[Upload & View](#)



# Three Core dimensions of LTTSiDrive™



**High  
Performance  
Compute-Stack**



**Virtual  
Development**



**Tool Chains,  
Digital Twins &  
Libraries**





**Purposeful.  
Agile.  
Innovation.**



**Thank You**